## **WEST Search History**

Hide Items Restore Clear Cancel

DATE: Monday, September 18, 2006

Hide?	<u>Set</u> Name	Query	<u>Hit</u> <u>Count</u>
	DB=PC	GPB, USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ	
	L19	L18 and (emulat\$ same request?)	22
	L18	L17 and (emulat\$ same instruction?)	35
Ĺ	L17	L16 and processor	108
	L16	emulat\$ and IOP and instruction? and request? and operating system	108
	L15	L13 and (instruction? with native)	3
	L14	L13 and IO	2
	L13	L7 and (operating system same emulat\$)	32
	L12	L7 and (IO with request?)	0
	L11	L7 and (first with instruction processor)	7
	L10	L7 and ((first with type)with instruction processor)	0
	L9	L7 and (input/output with request?)	5
	L8	L7 and IO request?	0
	L7	L5 and (emulat\$ same instruction?)	73
	L6	L5 and (emulat\$ same IO request?)	0
	L5	emulat\$ and IOP	269
	L4	(emulat\$ with instruction?) and (emulat\$ same IO request?)	0
	L3	(emulat\$ with IOP) and (instruction? with instruction processor?) and (emulat\$ same IO request?)	0
	L2	crandall.in. and emulat\$ and IOP	2
	L1	crandall.in. and (emulat\$ with input-output processor)	0

**END OF SEARCH HISTORY** 

## **Hit List**

First Hit Clear Generate Collection Print Ewd Refs Bkwd Refs

Generate OACS

Search Results - Record(s) 1 through 20 of 22 returned.

□ 1. Document ID: US 20060015296 A1

L19: Entry 1 of 22

File: PGPB

Jan 19, 2006

PGPUB-DOCUMENT-NUMBER: 20060015296

PGPUB-FILING-TYPE:

DOCUMENT-IDENTIFIER: US 20060015296 A1

TITLE: Information processing system and method

PUBLICATION-DATE: January 19, 2006

'INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

Aoki; Yoshitaka

Odawara

JΡ

Soejima; Kenichi

Odawara

JP

US-CL-CURRENT: <u>702/185</u>

Full	Title	Citation	Front	Review	Classification	Date Reference	Sequences	Attachments	Claims	KMAC	Draw, D
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2. Document ID: US 20020168966 A1

L19: Entry 2 of 22

File: PGPB

Nov 14, 2002

PGPUB-DOCUMENT-NUMBER: 20020168966

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020168966 A1

TITLE: I/O unit emulation

PUBLICATION-DATE: November 14, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

Tillier, Fabian S.

Beaverton

OR

US

US-CL-CURRENT: 455/412.1

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw. De

☐ 3. Document ID: US 20020026604 A1

L19: Entry 3 of 22

File: PGPB

Feb 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020026604

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020026604 A1

TITLE: Fault resilient/fault tolerant computing

PUBLICATION-DATE: February 28, 2002

· INVENTOR-INFORMATION:

NAME CITY STATE COUNTRY

Bissett, Thomas D. Kingston RΙ US Leveille, Paul A. Grafton MA US Muench, Erik Groveland MA US

US-CL-CURRENT: 714/12

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	K0040	Draw, D
•								2.2.12.2.1.2.2.2.	, 1000-01 HTP21 100	21211112	140012	6139% C

4. Document ID: US 6971046 B1

L19: Entry 4 of 22

File: USPT Nov 29, 2005

US-PAT-NO: 6971046

DOCUMENT-IDENTIFIER: US 6971046 B1

TITLE: System and method for performing input/output diagnostics

DATE-ISSUED: November 29, 2005

INVENTOR-INFORMATION:

NAME CITY ZIP CODE STATE COUNTRY

Johnson; Craig B. Shoreview MN Crandall; Carl R. Hugo MN

Park; Haeng D. St. Paul MN

US-CL-CURRENT: 714/35; 714/25

Full	Title	Citation	Front	Review	Classification	Date	Reference	ME THE THE	Philippine 22	Claims	KWAC	Draw, De

5. Document ID: US 6745320 B1

L19: Entry 5 of 22 File: USPT Jun 1, 2004

US-PAT-NO: 6745320

DOCUMENT-IDENTIFIER: US 6745320 B1

Record List Display Page 3 of 11

TITLE: Data processing apparatus

DATE-ISSUED: June 1, 2004

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Mitsuishi; Naoki Kodaira . JP

US-CL-CURRENT: 712/225; 711/212, 711/214, 712/201, 712/202, 712/203, 712/209,

<u>712/227</u>, <u>712/245</u>

Full Title Citation Front Review Classification Date Reference State Company Claims KNNC Draw, De

6. Document ID: US 6473869 B2

L19: Entry 6 of 22 File: USPT Oct 29, 2002

US-PAT-NO: 6473869

DOCUMENT-IDENTIFIER: US 6473869 B2

TITLE: Fault resilient/fault tolerant computing

DATE-ISSUED: October 29, 2002

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Bissett; Thomas D. Kingston RI Leveille; Paul A. Grafton MA Muench; Erik Groveland MA

US-CL-CURRENT: 714/12; 712/214, 713/375, 713/501

Full Title Citation Front Review Classification Date Reference Services & Chairs Kimo Draw. De

7. Document ID: US 6421742 B1

L19: Entry 7 of 22 File: USPT Jul 16, 2002

US-PAT-NO: 6421742

DOCUMENT-IDENTIFIER: US 6421742 B1

TITLE: Method and apparatus for emulating an input/output unit when transferring

data over a network

DATE-ISSUED: July 16, 2002

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Tillier; Fabian S. Beaverton OR

Record List Display Page 4 of 11

US-CL-CURRENT: 710/1; 709/201, 709/232, 710/11, 710/33, 710/61

Full Title Citation Front Review Classification Date Reference

□ 8. Document ID: US 6279119 B1

L19: Entry 8 of 22

File: USPT

STATE

ZIP CODE

Aug 21, 2001

COUNTRY

US-PAT-NO: 6279119

DOCUMENT-IDENTIFIER: US 6279119 B1

TITLE: Fault resilient/fault tolerant computing

DATE-ISSUED: August 21, 2001

INVENTOR-INFORMATION:

NAME

Bissett; Thomas D. Kingston RI

CITY

Leveille; Paul A.

Grafton MA

Muench; Erik

Groveland MA

US-CL-CURRENT: <u>714/12</u>; <u>703/23</u>, <u>712/229</u>, <u>713/501</u>

Full Title Citation Front Review Classification Date Reference Section 25 Claims KMC Draw De

☐ 9. Document ID: US 5991757 A

L19: Entry 9 of 22

File: USPT

Nov 23, 1999

US-PAT-NO: 5991757

DOCUMENT-IDENTIFIER: US 5991757 A

TITLE: Method and system for searching an array for an array value

DATE-ISSUED: November 23, 1999

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Dahl; Stephen A. Rochester MN
Endicott; John C. Rochester MN
Heyrman; Peter J. Rochester MN

Kirkman; R. Karl Rochester MN Mustain; Richard G. Rochester MN Peterson; Jon H. Rochester MN

US-CL-CURRENT: <u>707/3</u>; <u>703/26</u>, <u>707/100</u>

Full Title Citation Front Review Classification Date Reference

☐ 10. Document ID: US 5949985 A

L19: Entry 10 of 22

File: USPT

Sep 7, 1999

US-PAT-NO: 5949985

DOCUMENT-IDENTIFIER: US 5949985 A

TITLE: Method and system for handling interrupts during emulation of a program

DATE-ISSUED: September 7, 1999

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Dahl; Stephen A. Rochester MN Endicott; John C. Rochester MN Heyrman; Peter J. Rochester MN Kirkman; R. Karl Rochester MN Mustain; Richard G. Rochester MN Peterson; Jon H. Rochester MN

US-CL-CURRENT: 703/26; 710/262, 712/218, 712/244, 717/134, 717/138

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments Claims	_KMCDrawDe

☐ 11. Document ID: US 5819063 A

L19: Entry 11 of 22

File: USPT

Oct 6, 1998

US-PAT-NO: 5819063

DOCUMENT-IDENTIFIER: US 5819063 A

\*\* See image for Certificate of Correction \*\*

TITLE: Method and data processing system for emulating a program

DATE-ISSUED: October 6, 1998

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Dahl; Stephen A. Rochester MN Endicott; John C. Rochester MN Heyrman; Peter J. Rochester MN Kirkman; R. Karl Rochester MN Mustain; Richard G. Rochester MN Peterson; Jon H. Rochester MN

US-CL-CURRENT: 703/27

Full Title Citation Front Review Classification Date Reference Sequences Attachners Claims KMC Draw Da

☐ 12. Document ID: US 5652914 A

L19: Entry 12 of 22

File: USPT

Jul 29, 1997

US-PAT-NO: 5652914

DOCUMENT-IDENTIFIER: US 5652914 A

TITLE: Method and system for superimposing, creating and altering I/O applications and controls within an I/O subsystem by using an I/O subchannel intercept field

DATE-ISSUED: July 29, 1997

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Eckert; Wolfgang Altdorf DE

Halma; Marten Jan Poughquag NY

Maergner; Juergen Erwin Sindelfingen DE

Trotter; John Scott Pleasant Valley NY

US-CL-CURRENT: <u>710/36</u>; <u>710/48</u>

Full Title Citation Front Review Classification Date Reference State Section States Claims KNNC Draw, De

☐ 13. Document ID: US 5153577 A

L19: Entry 13 of 22

File: USPT Oct 6, 1992

US-PAT-NO: 5153577

DOCUMENT-IDENTIFIER: US 5153577 A

TITLE: Mapping character color attributes into grey pixel patterns

DATE-ISSUED: October 6, 1992

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Mackey; Kevin J. Palo Alto CA Woods; Donald R. Los Altos CA

US-CL-CURRENT: 345/639; 345/600, 345/629, 345/636, 345/694

Full Title Citation Front Review Classification Date Reference Company Section Claims KWIC Draw De

☐ 14. Document ID: US 5113517 A

L19: Entry 14 of 22 File: USPT May 12, 1992

US-PAT-NO: 5113517

DOCUMENT-IDENTIFIER: US 5113517 A

Record List Display Page 7 of 11

TITLE: Concurrent display of data from two different <u>processors</u> each having different display font and user interface for controlling transfer of converted font data therebetween

DATE-ISSUED: May 12, 1992

#### INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Beard; Marian H.	Sunnyvale	CA		
Caro; Perry A.	Palo Alto	CA		
Hsiao; Jennifer B.	San Jose	CA		
Mackey; Kevin J.	Palo Alto	CA		
Sandman, Jr.; James G.	Palo Alto	CA		
Steinbach; Gary R.	Mountain View	CA		
Woods; Donald R.	Los Altos	CA		

US-CL-CURRENT:  $\underline{703}/\underline{23}$ ;  $\underline{345}/\underline{502}$ ,  $\underline{345}/\underline{504}$ ,  $\underline{345}/\underline{543}$ ,  $\underline{715}/\underline{781}$ ,  $\underline{715}/\underline{804}$ ,  $\underline{715}/\underline{839}$ 

Full	Title	Citation F	ront	Review	Classification	Date	Reference	Englished Ministry	Claims	KWAC	Draw, D
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	15.	Documer	nt ID:	US 5	088033 A						

US-PAT-NO: 5088033

DOCUMENT-IDENTIFIER: US 5088033 A

\*\* See image for Certificate of Correction \*\*

TITLE: Data processing system  $\underline{\text{emulation}}$  in a window with a coprocessor and I/O  $\underline{\text{emulation}}$ 

DATE-ISSUED: February 11, 1992

#### INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP	CODE	COUNTRY
Binkley; Joseph H.	Palo Alto	CA			
Caro; Perry A.	Palo Alto	CA			
Dillon; John B.	Palo Alto	CA			
Fay; Charles R.	Long Beach	CA			
Gibbons; Jonathan	Mountain View	CA			
Hooks; Hilary N.	Newark	CA			
Kadifa; Abdo G.	Sunnyvale	CA			
Lee; Jeffery W.	Sunnyvale	CA			
Lynch; William C.	Palo Alto	CA			
Mock; Clayton W.	Redwood City	CA			
Neely; Everett T.	Montara	CA			
Tallan; Michael L.	Mountain View	CA			
Thompson; Geoffrey O.	Palo Alto	CA			
Vukkadala; Gaya	Sunnyvale	CA			

Record List Display Page 8 of 11

Wick; John D.

Palo Alto

CA

Woods; Donald R.

Los Altos

CA

US-CL-CURRENT: 703/24

Full	Title	Citation	Front	Review	Classification	Date	Reference	\$ 4.5 A.2	San Hade	Claims	KOMC	Draw, De

#### ☐ 16. Document ID: US 5062042 A

L19: Entry 16 of 22

File: USPT

Oct 29, 1991

US-PAT-NO: 5062042 `

DOCUMENT-IDENTIFIER: US 5062042 A

TITLE: System for managing data which is accessible by file address or disk address

via a disk track map

DATE-ISSUED: October 29, 1991

#### INVENTOR-INFORMATION:

NAME CITY ZIP CODE STATE COUNTRY Binkley; Joseph H. Palo Alto CA Caro; Perry A. Palo Alto CA Fay; Charles R. Long Beach CA Lee; Jeffery W. Sunnyvale CA Neely; Everett T. Montara CA Thompson; Geoffrey O. Palo Alto CA Vukkadala; Gaya Sunnyvale CA

US-CL-CURRENT: 707/205

Full	Title	Citation	Front	Review	Classification	Date	Reference	450	and the same	Claims	K0000	Draw, Dr

#### ☐ 17. Document ID: US 4939507 A

L19: Entry 17 of 22

File: USPT

Jul 3, 1990

US-PAT-NO: 4939507

DOCUMENT-IDENTIFIER: US 4939507 A

TITLE: Virtual and <a href="mailto:emulated">emulated</a> objects for use in the user interface of a display

screen of a display processor

DATE-ISSUED: July 3, 1990

#### INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Beard; Marian H. Sunnyvale CA Caro; Perry A. Palo Alto CA Hsiao; Jennifer B.

Mackey; Kevin J.

San Jose
CA
Palo Alto
CA
Sandman, Jr.; James G.
Palo Alto
CA
Steinbach; Gary R.

Mountain View
CA
Woods; Donald R.

Los Altos
CA

US-CL-CURRENT: 345/156; 703/21, 703/22, 703/26, 703/27, 715/804, 715/839

# Full Title Citation Front Review Classification Date Reference Sequences Attachments: Claims KMC Draw. Da

18. Document ID: US 4937036 A

L19: Entry 18 of 22

File: USPT

Jun 26, 1990

US-PAT-NO: 4937036

DOCUMENT-IDENTIFIER: US 4937036 A

TITLE: Concurrent display of data from two different display processors and user

1

interface therefore

DATE-ISSUED: June 26, 1990

INVENTOR-INFORMATION:

CITY NAME STATE ZIP CODE COUNTRY Beard; Marian H. Sunnyvale CA Caro; Perry A. Palo Alto CA Hsiao; Jénnifer B. San Jose CA Mackey; Kevin J. Palo Alto CA Sandman, Jr.; James G. Palo Alto CA Steinbach; Gary R. Mountain View CA Woods; Donald R. Los Altos CA

US-CL-CURRENT: <u>345/156</u>; <u>703/21</u>, <u>703/22</u>, 703/26, 703/27

Full	Title	Citation	Front	Review	Classification	Date	Reference	After more	Claims	KWIC	Draw, Dr
								 The second secon	0.0	144410	C IIDVW

#### ☐ 19. Document ID: US 4920481 A

L19: Entry 19 of 22

File: USPT

Apr 24, 1990

US-PAT-NO: 4920481

DOCUMENT-IDENTIFIER: US 4920481 A

TITLE: Emulation with display update trapping

DATE-ISSUED: April 24, 1990

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Binkley; Joseph H.	Palo Alto	CA
Caro; Perry A.	Palo Alto	CA
Dillon; John B.	Palo Alto	CA
Fay; Charles R.	Long Beach	CA
Gibbons; Jonathan	Mountain View	CA
Hooks; Hilary N.	Newark	CA
Kadifa; Abdo G.	Palo Alto	CA
Lee; Jeffery W.	Palo Alto	CA
Lynch; William C.	Palo Alto	CA
Mock; Clayton W.	Redwood City	CA
Neely; Everett T.	Montara	CA
Tallan; Michael L.	Mountain View	CA
Thompson; Geoffrey O.	Palo Alto	CA
Vukkadala; Gaya	Sunnyvale	CA
Wick; John D.	Palo Alto	CA
Woods; Donald R.	Los Altos	CA

US-CL-CURRENT: 703/26

Full	Title	Citation	Front	Review	Classification	Date	Reference	EROPERSON PROGRESS CI	laims	KWIC	Drawe D

## ☐ 20. Document ID: US 4899136 A

L19: Entry 20 of 22

File: USPT

Feb 6, 1990

US-PAT-NO: 4899136

DOCUMENT-IDENTIFIER: US 4899136 A

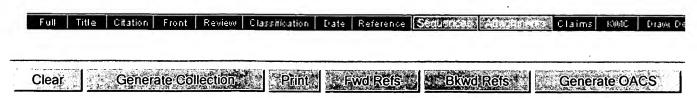
TITLE: Data processor having a user interface display with metaphoric objects

DATE-ISSUED: February 6, 1990

#### INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP	CODE	COUNTRY
Beard; Marian H.	Sunnyvale	CA			
Caro; Perry A.	Palo Alto	CA			
Hsiao; Jennifer B.	San Jose	CA			
Mackey; Kevin J.	Palo Alto	CA			
Sandman, Jr.; James G.	Palo Alto	CA			
Steinbach; Gary R.	Mountain View	CA			
Woods; Donald R.	Los Altos	CA			

US-CL-CURRENT: <u>345/156</u>; <u>400/76</u>, <u>715/804</u>, <u>715/839</u>



## **Hit List**

First Hit	Clear	Generate Collection Print	Fwd Refs	Bkwd Refs
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**Search Results** - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 7058932 B1

L15: Entry 1 of 3

File: USPT

Jun 6, 2006

US-PAT-NO: 7058932

DOCUMENT-IDENTIFIER: US 7058932 B1

TITLE: System, computer program product, and methods for emulation of computer

programs

DATE-ISSUED: June 6, 2006

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Jennings; Andrew T. West Chester PΑ US Krablin; G. Lawrence Downingtown PΑ US Fender; Timothy Neilson Berwyn PA US Stratton; William Malvern PA US

US-CL-CURRENT: 717/138; 717/139, 717/146, 717/152

Full Title Citation Front Review Classification Date Reference	Claims KNNC Draw D
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2. Document ID: US 6978233 B1

L15: Entry 2 of 3

File: USPT

Dec 20, 2005

US-PAT-NO: 6978233

DOCUMENT-IDENTIFIER: US 6978233 B1

TITLE: Method for emulating multi-processor environment

DATE-ISSUED: December 20, 2005

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE

COUNTRY

Burns; John J.

Bountiful

UT

US-CL-CURRENT: 703/27; 703/21, 703/23, 712/208, 712/213, 712/227, 712/41

Full Title Citation Front Review Classification Date Reference Section 1 A Section Claims KNMC Draws De

#### 3. Document ID: US 6128679 A

L15: Entry 3 of 3

File: USPT

Oct 3, 2000

US-PAT-NO: 6128679

DOCUMENT-IDENTIFIER: US 6128679 A

TITLE: Computer for executing I/O instructions, by emulation, in a foreign application program wherein the emulator result thread is blocked from running between processes

DATE-ISSUED: October 3, 2000

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE COUNTRY

Beale; Andrew Ward

Aliso Viejo

CA

Lee; Bong Jae

Laguna Niguel

CA

Ebersole; Dwayne Eugene

Lancaster

PA

US-CL-CURRENT: 710/62; 709/230, 709/249, 710/11, 710/63, 719/330

	Bkwd Refs Generate
Term	Documents
NATIVE	126181
NATIVES	168
INSTRUCTION?	0
INSTRUCTIONA	3
INSTRUCTIONB	1
INSTRUCTIONG	5
INSTRUCTIONI	1
INSTRUCTIONJ	1
INSTRUCTIONL	2
INSTRUCTIONN	12
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- Broaden your search by selecting other <u>databases</u>, removing limits, or searching "Citations and document text" (if available).
- Use "AND" to connect two words that don't need to be searched as a phrase.
- Connect similar terms with the "OR" operator (e.g. military OR pentagon). See Search Tips for more hints.

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Input output AND Product introduct	tion	Computer Technology Review; Los Ang	eles
Input output AND Standards		Computerworld; Framingham	
		InfoWorld; San Mateo	

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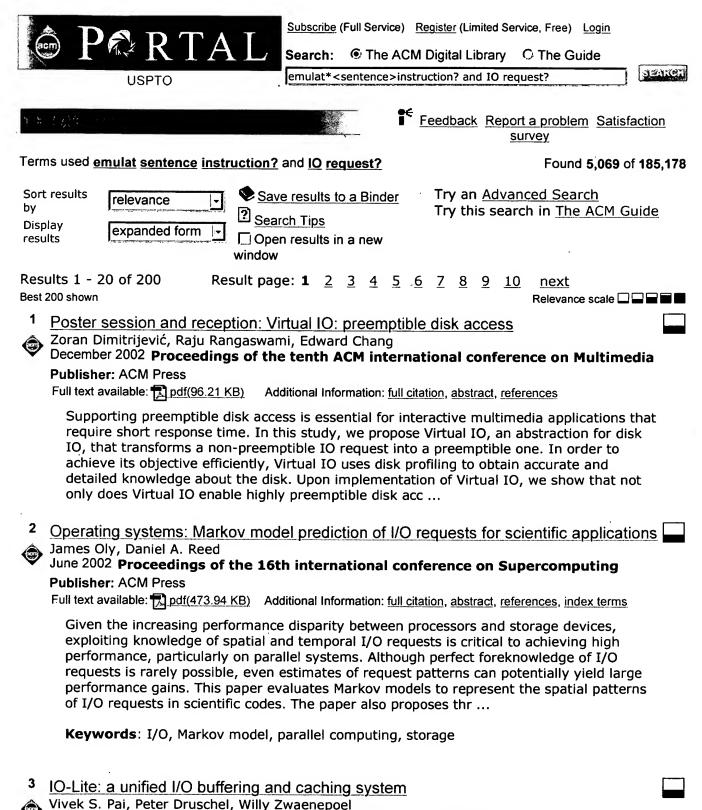
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February 2000 ACM Transactions on Computer Systems (TOCS), Volume 18 Issue 1
Publisher: ACM Press

Full text available: pdf(196.15 KB)

Additional Information: full citation, abstract, references, citings, index terms

This article presents the design, implementation, and evaluation of IO -Lite, a unified I/O buffering and caching system for general-purpose operating systems. IO-Lite unifies all

buffering and caching in the system, to the extent permitted by the hardware. In particular, it allows applications, the interprocess communication system, the file system, the file cache, and the network subsystem to safely and concurrently share a single physical copy of the data. Protection and ...

**Keywords**: I/O buffering, caching, networking, zero-copy

On implementing MPI-IO portably and with high performance Rajeev Thakur, William Gropp, Ewing Lusk May 1999 Proceedings of the sixth workshop on I/O in parallel and distributed Publisher: ACM Press Full text available: pdf(887.89 KB) Additional Information: full citation, references, citings, index terms MPI-IO/GPFS, an optimized implementation of MPI-IO on top of GPFS Jean-Pierre Prost, Richard Treumann, Richard Hedges, Bin Jia, Alice Koniges November 2001 Proceedings of the 2001 ACM/IEEE conference on Supercomputing (CDROM) Publisher: ACM Press Full text available: pdf(168.17 KB) Additional Information: full citation, abstract, references, index terms MPI-IO/GPFS is an optimized prototype implementation of the I/O chapter of the Message Passing Interface (MPI) 2 standard. It uses the IBM General Parallel File System (GPFS) Release 3 as the underlying file system. This paper describes optimization features of the prototype that take advantage of new GPFS programming interfaces. It also details how collective data access operations have been optimized by minimizing the number of messages exchanged in sparse accesses and by increasing the overla ... Keywords: GPFS, MPI-IO, SMP node, benchmark, data shipping, double buffering, file hints, optimization, performance, prefetching Disk array performance in a random IO environment T. M. Oslon September 1989 ACM SIGARCH Computer Architecture News, Volume 17 Issue 5 **Publisher: ACM Press** Full text available: 🙀 pdf(441.34 KB) Additional Information: full citation, abstract, citings, index terms Large arrays of disks have been proposed as a way to meet the need for increasing IO bandwidth. This paper examines disk array performance in a random IO environment. It also presents the results of performance testing using the Prime IOBENCH™ benchmark on a combination of disk striping, RAID 1, and RAID 5 disk arrays. It concludes, that for a given number of disk drives, that both RAID 1 and RAID 5 have acceptable performance in a read environment, while RAID 5 degrades significantly ... <sup>7</sup> Manageability: Request extraction in Magpie: events, schemas and temporal joins Rebecca Isaacs, Paul Barham, James Bulpin, Richard Mortier, Dushyanth Narayanan September 2004 Proceedings of the 11th workshop on ACM SIGOPS European workshop: beyond the PC EW11 Publisher: ACM Press Full text available: pdf(223.72 KB) Additional Information: full citation, abstract, references

This paper addresses the problem of extracting individual request activity from interleaved event traces. We present a new technique for event correlation which applies a form of

temporal join over timestamped, parameterized event streams in order to identify the events pertaining to an individual request. Event schemas ensure that the request extraction mechanism applies to any server application or service without modification, and is robust against future changes in application ...

8 Industrial session: potpourri: Getting priorities straight: improving Linux support for database I/O Christoffer Hall, Philippe Bonnet

August 2005 Proceedings of the 31st international conference on Very large data bases VLDB '05

Publisher: VLDB Endowment

Full text available: 📆 pdf(349.39 KB) Additional Information: full citation, abstract, references, index terms

The Linux 2.6 kernel supports asynchronous I/O as a result of propositions from the database industry. This is a positive evolution but is it a panacea? In the context of the Badger project, a collaboration between MySQL AB and University of Copenhagen, we evaluate how MySQL/InnoDB can best take advantage of Linux asynchronous I/O and how Linux can help MySQL/InnoDB best take advantage of the underlying I/O bandwidth. This is a crucial problem for the increasing number of MySQL servers deployed ...

N-body simulation of galaxy formation on GRAPE-4 special-purpose computer Toshiyuki Fukushiqe, Junichiro Makino

November 1996 Proceedings of the 1996 ACM/IEEE conference on Supercomputing (CDROM) Supercomputing '96

Publisher: IEEE Computer Society

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(255.94 KB) terms

We report on resent N-body simulations of galaxy formation performed on the GRAPE-4 (GRAvity PipE 4) system, a special-purpose computer for astrophysical N-body simulations. We review the astrophysical motivation, the algorithm, the actual performance, and the price per performance. The performance obtained is 332 Gflops averaged over 185 hours for a simulation of a galaxy formation with 786,400 particles. The price per performance obtained is 4,600 dollars per Gflops. The configuration use ...

ARIMA time series modeling and forecasting for adaptive I/O prefetching

Nancy Tran, Daniel A. Reed

June 2001 Proceedings of the 15th international conference on Supercomputing

**Publisher: ACM Press** 

Full text available: pdf(485.27 KB)

Additional Information: full citation, abstract, references, citings, index terms

Bursty application I/O patterns, together with transfer limited storage devices, combine to create a major I/O bottleneck on parallel systems. This paper explores the use of time series models to forecast application I/O request times, then prefetching I/O requests during computation intervals to hide I/O latency. Experimental results with I/O intensive scientific codes show performance improvements compared to standard UNIX prefetching strategies.

Keywords: I/O, access pattern, modeling, prefetching, times series

11 The performance impact of kernel prefetching on buffer cache replacement algorithms

Ali R. Butt, Chris Gniady, Y. Charlie Hu

June 2005 ACM SIGMETRICS Performance Evaluation Review, Proceedings of the 2005 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '05, Volume 33 Issue 1

Publisher: ACM Press

Full text available: pdf(303.47 KB) Additional Information: full citation, abstract, references, index terms

A fundamental challenge in improving the file system performance is to design effective block replacement algorithms to minimize buffer cache misses. Despite the well-known interactions between prefetching and caching, almost all buffer cache replacement algorithms have been proposed and studied comparatively without taking into account file system prefetching which exists in all modern operating systems. This paper shows that such kernel prefetching can have a significant impact on the relative ...

**Keywords**: buffer caching, prefetching, replacement algorithms

12 Parallel simulation of parallel file systems and I/O programs

Rajive Bagrodia, Stephen Docy, Andy Kahn November 1997 Proceedings of the 1997 ACM/IEEE conference on Supercomputing (CDROM)

Publisher: ACM Press

Full text available: pdf(245.25 KB) Additional Information: full citation, abstract, references, citings

Efficient I/O implementations can have a significant impact on the performance of parallel applications. This paper describes the design and implementation of PIOSIM, a parallel simulation library for MPI-IO programs. The simulator can be used to predict the performance of existing MPI-IO programs as a function of architectural characteristics, caching algorithms, and alternative implementations of collective I/O operations. This paper describes the simulator and presents the results of a number ...

13 Estimation and design techniques for energy-efficient memory systems: Energy-

efficient flash-memory storage systems with an interrupt-emulation mechanism Chin-Hsien Wu, Tei-Wei Kuo, Chia-Lin Yang

September 2004 Proceedings of the 2nd IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis

Publisher: ACM Press

Full text available: pdf(179.27 KB) Additional Information: full citation, abstract, references, index terms

One of the emerging critical issues for flash-memory storage systems, especially on the implementations of many embedded systems, is on its programmed I/O nature for data transfers. Programmed-I/O-based data transfers might not only result in the wasting of valuable CPU cycles of microprocessors but also unnecessarily consume much more energy from batteries. This paper presents an interrupt-emulation mechanism for flash-memory storage systems with an energy-efficient management strategy. We prop ...

**Keywords**: embedded systems, energy-efficient, flash memory, interrupt-emulation I/O, programmed I/O, storage systems

14 Performance of a DECnet based disk block server

Rollins Turner, Jeffrey Schriesheim, Indrajit Mitra

January 1984 ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1984 ACM SIGMETRICS conference on Measurement and modeling of computer systems SIGMETRICS '84, Volume 12 Issue 3

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available:

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terms

This report describes an experimental disk block server implemented for the RSX-11M Operating System using DECnet. The block server allows user programs on one system to access files on a disk physically located on a different system. The actual interface is at the level of physical blocks and IO transfers. Results of basic performance measurements are given, and explained in terms of major components. Performance predictions are made for servers of this type supporting more complex workloa ...

15 A case for using MPI's derived datatypes to improve I/O performance

Rajeev Thakur, William Gropp, Ewing Lusk

November 1998 Proceedings of the 1998 ACM/IEEE conference on Supercomputing (CDROM)

Publisher: IEEE Computer Society

Full text available: html(42.24 KB) Additional Information: full citation, abstract, references, citings

MPI-IO, the I/O part of the MPI-2 standard, is a promising new interface for parallel I/O. A key feature of MPI-IO is that it allows users to access several noncontiquous pieces of data from a file with a single I/O function call by defining file views with derived datatypes. We explain how critical this feature is for high performance, why users must create and use derived datatypes whenever possible, and how it enables implementations to perform optimizations. In particular, we describe two op ...

Keywords: MPI-IO, parallel I/O

16 D-SPTF: decentralized request distribution in brick-based storage systems

Christopher R. Lumb, Richard Golding

October 2004 ACM SIGPLAN Notices, ACM SIGARCH Computer Architecture News, **ACM SIGOPS Operating Systems Review , Proceedings of the 11th** international conference on Architectural support for programming languages and operating systems ASPLOS-XI, Volume 39, 32, 38 Issue 11, 5, 5

Publisher: ACM Press

Full text available: pdf(328.72 KB) Additional Information: full citation, abstract, references, index terms

Distributed Shortest-Positioning Time First (D-SPTF) is a request distribution protocol for decentralized systems of storage servers. D-SPTF exploits high-speed interconnects to dynamically select which server, among those with a replica, should service each read request. In doing so, it simultaneously balances load, exploits the aggregate cache capacity, and reduces positioning times for cache misses. For network latencies expected in storage clusters (e.g., 10--200µs), D-SPTF performs as ...

Keywords: brick based storage, decentralized systems, disk scheduling, distributed systems, storage systems

17 Input/output characteristics of scalable parallel applications

Phyllis E. Crandall, Ruth A. Aydt, Andrew A. Chien, Daniel A. Reed

December 1995 Proceedings of the 1995 ACM/IEEE conference on Supercomputing (CDROM) - Volume 00 Supercomputing '95

Publisher: ACM Press, IEEE Computer Society

html(2.96 KB)

Publisher Site

Full text available: 🔁 pdf(2.11 MB) 🐠

Additional Information: full citation, abstract, references, citings, index terms

Rapid increases in computing and communication performance are exacerbating the longstanding problem of performance-limited input/output. Indeed, for many otherwise

scalable parallel applications. input/output is emerging as a major performance bottleneck. The design of scalable input/output systems depends critically on the input/output requirements and access patterns for this emerging class of large-scale parallel applications. However, hard data on the behavior of such applications is only ...

18 <u>BubbleUp: low latency fast-scan for media servers</u> Edward Chang, Hector Garcia-Molina November 1997 Proceedings of the fifth ACM international conference on Multimedia Publisher: ACM Press Full text available: pdf(1.93 MB) Additional Information: full citation, references, citings, index terms Keywords: disk scheduling, initial latency, memory utilization, multimedia 19 Systems programming in concurrent prolog Ehud Shapiro January 1984 Proceedings of the 11th ACM SIGACT-SIGPLAN symposium on Principles of programming languages Publisher: ACM Press Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.12 MB) terms Concurrent Prolog [28] combines the logic programming computation model with guardedcommand indeterminacy and dataflow synchronization. It will form the basis of the Kernel Language [21] of the Parallel Inference Machine [36], planned by Japan's Fifth Generation Computers Project. This paper explores the feasibility of programming such a machine solely in Concurrent Prolog (in the absence of a lower-level programming language), by implementing in it a representative collection of systems p ... 20 The SGI Origin: a ccNUMA highly scalable server James Laudon, Daniel Lenoski May 1997 ACM SIGARCH Computer Architecture News, Proceedings of the 24th annual international symposium on Computer architecture ISCA '97, Volume 25 Issue 2 Publisher: ACM Press Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.74 MB) terms The SGI Origin 2000 is a cache-coherent non-uniform memory access (ccNUMA) multiprocessor designed and manufactured by Silicon Graphics, Inc. The Origin system was designed from the ground up as a multiprocessor capable of scaling to both small and large processor counts without any bandwidth, latency, or cost cliffs. The Origin system consists of up to 512 nodes interconnected by a scalable Craylink network. Each node consists of one or two R10000 processors, up to 4 GB of coherent memory, and ... Results 1 - 20 of 200 Result page: **1** <u>2</u> <u>3</u> <u>4</u> <u>5</u> <u>6</u> <u>7</u> <u>8</u> <u>9</u> <u>10</u>

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